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Customer No.: 31561
Docket No.: 11644-US-PA
Application No.: 10/605,236

first switch comprises a first transistor and a pull-up resistor, the second switch comprises a second transistor and a first pull-down transistor, and the third switch comprises a third transistor and a second pull-down transistor.

14. (original) The data-exchangeable USB device of claim 10, wherein the function module comprises:

- a control device, for controlling circuits of the data-exchangeable USB device;
- a buffer device, electrically connected to the control device and the USB interface module, for temporarily storing data; and
- a mass storage device, electrically connected to the buffer device, for storing or retrieving data.

15. (original) The data-exchangeable USB device of claim 14, wherein the mass storage device comprises:

- a nonvolatile storage media, for storing data; and
- a storage interface, electrically connected to the nonvolatile storage media, for accessing the nonvolatile storage media.

16. (original) The data-exchangeable USB device of claim 15, wherein the nonvolatile storage media is comprised of a flash memory.

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17. (original) The data-exchangeable USB device of claim 14, wherein the function module further comprises a digital-to-analog (A/D) codec electrically connected to the buffer device, used to convert digital data to analog data while the data-exchangeable USB device is at a play-state.

18. (original) The data-exchangeable USB device of claim 17, wherein the A/D codec is a MP3 codec.

19. (original) The data-exchangeable USB device of claim 14, wherein the control device comprises:

- a CPU, used as a control center to the data-exchangeable USB device;
- a nonvolatile memory, electrically connected to the CPU, for storing a driver program of the data-exchangeable USB device; and
- a volatile memory, electrically connected to the CPU, for temporarily storing CPU data.

20. (original) The data-exchangeable USB device of claim 14, wherein the buffer device further comprises a first buffer, a second buffer, and a third buffer in which the data-exchangeable USB device first reads a file allocation table (FAT) from the another USB device to the first buffer, according to the FAT stored in the first buffer, data stored in a data block of the another USB device is read and stored to the second buffer, then, while consuming data stored in the second buffer, another data block of the another USB device is read and stored

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to the third buffer, further, while consuming data stored in the third buffer, another data block of the another USB device is read and stored to the second buffer, by repeating a read-and-consume procedure described above, data stored in the another USB device is consumed continuously on the data-exchangeable USB device, and during a play pause, all related data blocks of the another USB device are read and stored to the mass storage device.